
whoami

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- Firmware Engineer working primarily with coreboot and EDK2
- Have been at 3mdeb for 3 years now
- Free and open source software enthusiast

Agenda

- What do we need to be able to run coreboot?
- What is Platform Secure Boot?
- How do I check if I have it?
- Demo
- Future plans
- Discussion

Can x run coreboot?

Abridged checklist

1. Is there existing code for the silicon in question?
 - check coreboot/src/soc directory
2. Does x have any hardware RoT fused to vendor's keys?
 - On x86: Intel Boot Guard, AMD Platform Secure Boot
 - Can also be external to the silicon platform, e.g. BMC

Step 1 can be checked fairly easily, and because Google has many vendors for chromebooks and chromeboxes, mobile/laptop SoCs are often supported ahead of release

Step 2 is more tricky :)

What is AMD Platform Secure Boot?

- AMD's version of Intel Boot Guard
- In summary, AMD ASP is fused to only accept BIOS images signed by board vendor's private key
 - Fuses are on the CPU package itself, whereas on Intel they're in the PCH
 - This means that on socketed desktop platforms, bypassing PSB is as simple as replacing the CPU
 - Also means that once fused, a CPU will only be usable inside boards from that specific vendor, or even only in that device model
 - Kills used CPU market, so it's seldom used in enthusiast / DIY boards

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Workstation Workstation Processors

Lenovo Vendor Locking Ryzen-based Systems with AMD PSB

By **Patrick Kennedy** - December 31, 2021






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Source: ServeTheHome

How do I check if I have it?

- For Intel, we have Felix Singer's bootguard-status project: <https://github.com/felixsinger/bootguard-status>
- For AMD, there wasn't a comparable tool, so I wrote psb_status
 - Very small bash script (32 LoC)
 - Based on coreboot's implementation of PSB
 - Checks for FUSE_PLATFORM_SECURE_BOOT_EN bit in PSB_STATUS_OFFSET register
 - I also compile a list of user reports, so feel free to contribute :)

Make	Model	Status
Lenovo	ThinkPad L14 Gen2	Disabled
Lenovo	ThinkPad P14s Gen4	Enabled
Lenovo	ThinkPad T14 Gen1	Enabled
Valve	Steam Deck	Disabled

<input type="checkbox"/>	 0 Open	<input checked="" type="checkbox"/> 4 Closed
<input type="checkbox"/>	 Update README.md with Alienware R10 #4 by AlbertoHSande was merged on Oct 10	
<input type="checkbox"/>	 PSB not enabled on L14gen2 #3 by gilbert-fernandes was merged on Oct 8, 2023 • Approved	
<input type="checkbox"/>	 Add info about Gigabyte X570s Aero G #2 by Lawstorant was merged on Oct 7, 2023 • Approved	
<input type="checkbox"/>	 README.md: Add Asus Pro WS X570-ACE #1 by zsrv was merged on Oct 7, 2023 • Approved	

Future plans

- Make checking more reliable
 - One user reported discrepancy between psb_status and fwupd HSI
 - Check more status bits
- Make it easier to submit results
- Remove dependency on external tools (iotools)
- Offline analysis of BIOS binaries

The background is a dark gray with abstract, light gray lines in the corners that resemble circuit traces or a stylized grid. These lines are composed of straight segments and right-angle turns, with some ending in small circular nodes. They are located in the top-left, top-right, and bottom-right corners, framing the central text.

Discussion